PATENT APPLICATION

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H. Jey Tasai

109975

Group Art Unit:

Examiner:

Docket No.:

N THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Tatsuya SHIMODA et al.

Application No.: 09/892,872

Filed: June 28, 2001

FESSOELECTRIC MEMORY AND METHOD OF MAKING THE SAME

RESPONSE TO RESTRICTION REQUIREMENT

Director of the U.S. Patent and Trademark Office Washington, D.C. 20231

Sir:

For:

In response to the Restriction Requirement mailed February 15, 2002, Applicants hereby provisionally elect Group I, claims 1-11 and 20-23 drawn to a semiconductor device. This election is made with traverse.

It is respectfully submitted that the subject matter of all claims 1-25 is sufficiently related that a thorough search for the subject matter of any one group would necessarily encompass a search for the subject matter of the remaining groups. Thus, it is respectfully submitted that the search and examination of the entire application could be performed without serious burden. MPEP §803 states that "if the search and examination of the entire application can be made without serious burden, the Examiner must examine it on its merits, even though it includes claims to distinct or independent inventions". It is respectfully submitted that this policy should apply in the present application in order to avoid unnecessary delay and expense to Applicants and duplicative examination by the Patent Office.

Accordingly, the Examiner is respectfully requested to reconsider and withdraw the Restriction Requirement and to examine all claims in this application.

Respectfully submitted,

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JAO:CCH/pmo

Date: March 12, 2002

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